

CMI	Bit 0	Factory setting command (active at 1).
	Bit 1	Save configuration to EEPROM non-volatile memory command (active at 1). This Bit automatically changes to 0 after the request is taken into account. The command is only active if the drive is stopped, and not in "5-Operation enabled" state. Note: If CMI is a periodic network variable, the PLC program must write it to 0 after the first request is taken into account. The life of the EEPROM memory is limited to 100,000 write operations. Note: If the motor or configuration switching function is active, the configuration in the RAM is saved to the EEPROM in the configuration designated by [Config. Active] (CnFS).
	Bit 2	Reserved (= 0).
	Bit 3	Reserved (= 0).
	Bit 4	Reserved (= 0).
	Bit 5	Reserved (= 0).
	Bit 6	Reserved (= 0).
	Bit 7	Reserved (= 0).
	Bit 8	Reserved (= 0).
	Bit 9	Definition of the frequency reference (LFr) and output frequency (rFr) unit: = 0: 0.1 Hz = 1: Standardized value 16 signed bits based on the maximum frequency. The value 32767 corresponds to [Max frequency] (tFr). The default value of [Max frequency] (tFr) is 60 Hz, and the resolution is then approximately 0.0018 Hz. This function has no effect on the speed reference (LFRd) or the output speed (rFRd).
	Bit 10	Reserved (= 0).
	Bit 11	Reserved (= 0).
	Bit 12	Reserved (= 0).
	Bit 13	Reserved (= 0).
	Bit 14	Reserved (= 0).
	Bit 15	Parameter consistency check = 0: The check is activated. Each time a parameter is written, the drive checks the relationship between the written parameter and the configuration in the drive. For example, the [High speed] (HSP) parameter must be less than [Max frequency] (tFr). = 1: The check is deactivated. The drive is locked in stop mode. In this drive state, the configuration can be written parameter by parameter and the drive does not modify the values that are written. The switch from 1 to 0 triggers a calculation of the consistency of the configuration. Some parameters can be modified automatically by the drive.
CMD	Possible values in CIA402 profile, separate or not separate mode:	
CMP0	Bit 0	Switch on/Contactor command.
CMP1	Bit 1	Disable voltage/Authorization to supply AC power.
CMP2	Bit 2	Quick stop.
CMP3	Bit 3	Enable operation/Run command.
CMP4	Bit 4	Reserved (set to 0).
CMP5	Bit 5	Reserved (set to 0).
CMP6	Bit 6	Reserved (set to 0).
CMP7	Bit 7	Fault reset/error cleared on transition 0 to 1.
CMP8	Bit 8	Halt Stop according to the [Type of stop] (Stt) parameter without leaving the Operation enabled state.
CMP9	Bit 9	Reserved (set to 0).
CMPA	Bit 10	Reserved (set to 0).
CMPB	Bit 11 to 15	Can be assigned to commands.
CMPC	Possible values in the IO profile on state command [2 wire] (2C).	
CMPD	Bit 0	Forward (on state) command = 0: No forward command = 1: Forward command The assignment of Bit 0 cannot be modified. It corresponds to the assignment of the terminals. It can be switched. Bit 0 (Cd00) is only active if the channel of this control word is active.
CMPE	Bit 1 to 15	Can be assigned to commands.
CMPF	Possible values in the IO profile on edge command [3 wire] (3C).	
	Bit 0	Stop (run authorization). = 0: Stop = 1: Run is authorized on a forward or reverse command
	Bit 1	Forward (on 0 to 1 rising edge) command. The assignment of Bits 0 and 1 cannot be modified. It corresponds to the assignment of the terminals. It can be switched. Bits 0 (Cd00) and 1 (Cd01) are only active if the channel of this control word is active.
	Bit 2 to 15	can be assigned to commands.
ETA	Possible values in CIA402 profile, separate or not separate mode:	
EP0	Bit 0	= 1: "Ready to switch on", awaiting power section line supply
EP1	Bit 1	= 1: "Switched on", ready
EP2	Bit 2	= 1: "Operation enabled", running
EP3	Bit 3	= 1: Fault detection
EP4	Bit 4	"Voltage enabled", power part connected to supply mains = 0: Power part not connected to supply mains = 1: Power part connected to supply mains
EP5		When the drive is powered by the power section only, this bit is always at 1.
EP6	Bit 5	= 0: Quick stop
EP7	Bit 6	= 1: "Switched on disabled", power section line supply locked
EP8	Bit 7	Bit 7: Warning = 0: No warning = 1: Warning active
EP9	Bit 8	Bit 8: Reserved (= 0)
EPA	Bit 9	Bit 9: Remote: command or reference via fieldbus = 0: Command or reference given via the graphic display terminal or remote display terminal = 1: Command or reference via fieldbus
EPB	Bit 10	Bit 10: Target reference reached = 0: The reference is not reached = 1: The reference has been reached When the drive is in speed mode, this is the reference frequency.
EPC	Bit 11	Bit 11: "Internal limit active", reference outside limits = 0: The reference is within the limits = 1: The reference is not within the limits When the drive is in speed mode, the limits are defined by the [Low speed] (LSP) and [High speed] (HSP) parameters.
EPD	Bit 12	Bit 12: Reserved (= 0)
EPE	Bit 13	Bit 13: Reserved (= 0)
EPF	Bit 14	Bit 14: "Stop key", STOP via stop key = 0: STOP key not active = 1: Stop triggered by the STOP key on the graphic display terminal or the remote display terminal
ETAD	Bit 15	Bit 15: "Direction", direction of rotation = 0: Forward rotation at output = 1: Reverse rotation at output The combination of Bits 0, 1, 2, 4, 5 and 6 defines the state in the DSP 402 state chart (refer to fieldbus manuals).
ETI	Bit 0	= 1: Access to the EEPROM non-volatile memory in progress
IP0	Bit 1	= 0: No parameter consistency check = 1: Parameter consistency check
IP1	Bit 2	= 0: The drive is not in operating state "Fault" or in operating state "Fault" and the error is active = 1: The drive is in operating state "Fault" and the error is no longer active (not reset)
IP2	Bit 3	Bit 3: Reserved (= 0).
IP3	Bit 4	= 1: The drive is in speed mode
IP4	Bit 5	= 1: DC injection active
IP5	Bit 6	= 0: Drive in steady state = 1: Drive in transient state
IP6	Bit 7	= 1: Motor thermal state threshold reached for the active motor
IP7	Bit 8	= 1: DC bus overvoltage
IP8	Bit 9	= 1: Acceleration active
IP9	Bit 10	= 1: Deceleration active
IPA	Bit 11	= 1: Current limit active
IPB	Bit 12	= 1: Fast stop active
IPC	Bit 13 to 14	bit 13 = 0 and bit 14 = 0: Drive controlled by terminal bit 13 = 1 and bit 14 = 0: Drive controlled by remote keypad bit 13 = 0 and bit 14 = 1: Drive controlled by Modbus bit 13 = 1 and bit 14 = 1: Drive controlled by CANopen or the network card
IPD	Bit 15	= 0: Forward operation applied before the ramp = 1: Reverse operation applied before the ramp
IPF		
CRC	Bit 0	= 1: Terminal board
	Bit 1	= 1: Local keypad
	Bit 2	= 1: Deported keypad
	Bit 3	= 1: Modbus
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	= 1: CANopen
	Bit 7	= 1: Terminal up-Down speed
	Bit 8	= 1: Deported keypad up-down speed
	Bit 9	= 1: COM option board
	Bit 10	Reserved

	Bit 11	= 1 : Embedded Ethernet
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	= 1 : Indus
	Bit 15	= 1 : PowerSuite
CCC	Bit 0	= 1 : Terminal board
	Bit 1	= 1 : Local keypad
	Bit 2	= 1 : Deported keypad
	Bit 3	= 1 : Modbus
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	= 1 : CANopen
	Bit 7	= 1 : Terminal up-Down speed
	Bit 8	= 1 : Deported keypad up-down speed
	Bit 9	= 1 : COM option board
	Bit 10	Reserved
	Bit 11	= 1 : Embedded Ethernet
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	= 1 : Indus
	Bit 15	= 1 : PowerSuite
IL1R	Bit 0	"LI1" logic inputs real image
	Bit 1	"LI2" logic inputs real image
	Bit 2	"LI3" logic inputs real image
	Bit 3	"LI4" logic inputs real image
	Bit 4	"LI5" logic inputs real image
	Bit 5	"LI6" logic inputs real image
	Bit 6	"LI7" logic inputs real image
	Bit 7	"LI8" logic inputs real image
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	"LI11" logic inputs real image
	Bit 11	"LI12" logic inputs real image
	Bit 12	"LI13" logic inputs real image
	Bit 13	"LI14" logic inputs real image
	Bit 14	"LI15" logic inputs real image
		Bit 15
OL1R	Bit 0	"R1" relay real image
	Bit 1	"R2" relay real image
	Bit 2	"R3" relay real image
	Bit 3	"R4" relay real image
	Bit 4	"R5" relay real image
	Bit 5	"R6" relay real image
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	"DO1" logic outputs real image
	Bit 9	"DO2" logic outputs real image
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	"DO11" logic outputs real image
	Bit 13	"DO12" logic outputs real image
	Bit 14	Reserved
		Bit 15
CIC	Bit 0	= 1 : Change of rating.
	Bit 1	= 1 : The fielbus module has been added.
	Bit 2	= 1 : The fielbus module has been removed.
	Bit 3	= 1 : Loaded config invalid.
	Bit 4	= 1 : The fielbus module has been changed.
	Bit 5	Reserved (=0).
	Bit 6	Reserved (=0).
	Bit 7	Reserved (=0).
	Bit 8	= 1 : The IO module has been added.
	Bit 9	= 1 : The IO module has been removed.
	Bit 10	= 1 : The IO module has been changed.
	Bit 11	= 1 : The encoder module has been added.
	Bit 12	= 1 : The encoder module has been removed.
	Bit 13	= 1 : The encoder module has been changed.
	Bit 14	= 1 : The control board has been changed.
		If one of these events occurs, the drive will trigger an error [Invalid configuration] (CFI) and then automatically restore the factory settings.
CRP0	Bit 0 to Bit 7: Active command channels	
CRP1	=0 : The terminal board is the active channel.	
CRP2	=1 : The local keypad is the active channel.	
CRP3	=2 : The remote keypad is the active channel.	
CRP4	=3 : Modbus is the active channel.	
CRP5	=4 : Reserved	
CRP6	=5 : Reserved	
CRP7	=6 : CANopen is the active channel.	
CRP8	=7 : The terminals are the active channel in the +/- speed reference.	
CRP9	=8 : The remote keypad is the active channel (up-down speed)	
CRPA	=9 : The fieldbus module is the active channel.	
CRPB	=10 : Reserved	
CRPC	=11 : Reserved	
CRPD	=12 : Reserved	
CRPE	=13 : Reserved	
CRPF	=14 : Reserved	
	Bit 8 to Bit 15: Active reference channels	
	=0 : The terminals are the active channel via an analog input.	
	=1 : The local keypad is the active channel.	
	=2 : The remote keypad is the active channel.	
	=3 : Modbus is the active channel.	
	=4 : Reserved	
	=5 : Reserved	
	=6 : CANopen is the active channel.	
	=7 : The terminals are the active channel in the +/- speed reference.	
	=8 : The remote keypad is the active channel (up-down speed)	
	=9 : The fieldbus module is the active channel.	
	=10 : Reserved	
	=11 : Reserved	
	=12 : Reserved	
	=13 : Reserved	
	=14 : Reserved	
	=15 : SoMove software is the active channel.	
FRY	Bit 0	= 1 : All parameters
	Bit 1	= 1 : Drive configuration
	Bit 2	Reserved
	Bit 3	= 1 : Motor parameters
	Bit 4	= 1 : Communication menu
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	= 1 : Display menu
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
		Bit 15
EWE	Bit 0	= 1 : Enable Web
	Bit 1	= 1 : Enable Email
IL11	Bit 0	"LI1" logic inputs physical image

	Bit 1	"LI2" logic inputs physical image
	Bit 2	"LI3" logic inputs physical image
	Bit 3	"LI4" logic inputs physical image
	Bit 4	"LI5" logic inputs physical image
	Bit 5	"LI6" logic inputs physical image
	Bit 6	"LI7" logic inputs physical image
	Bit 7	"LI8" logic inputs physical image
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	"LI11" logic inputs physical image
	Bit 11	"LI12" logic inputs physical image
	Bit 12	"LI13" logic inputs physical image
	Bit 13	"LI14" logic inputs physical image
	Bit 14	"LI15" logic inputs physical image
	Bit 15	"LI16" logic inputs physical image
PEV1	Bit 0	= 1 : first mapped Object
	Bit 1	= 1 : second mapped Object
	Bit 2	= 1 : third mapped Object
	Bit 3	= 1 : fourth mapped Object
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
PEV2	Bit 0	= 1 : first mapped Object
	Bit 1	= 1 : second mapped Object
	Bit 2	= 1 : third mapped Object
	Bit 3	= 1 : fourth mapped Object
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
PEV3	Bit 0	= 1 : first mapped Object
	Bit 1	= 1 : second mapped Object
	Bit 2	= 1 : third mapped Object
	Bit 3	= 1 : fourth mapped Object
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
PEV4	Bit 0	= 1 : first mapped Object
	Bit 1	= 1 : second mapped Object
	Bit 2	= 1 : third mapped Object
	Bit 3	= 1 : fourth mapped Object
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
ST00	Bit 0	(JAMP) : An anti-jam request is pending
	Bit 1	(JAMR) : An anti-jam sequence is running
	Bit 2	(FILL) : Pipe fill sequence is running
	Bit 3	(IPPC) : Inlet pressure compensation is running
	Bit 4	(SLM) : Stop asked by a "sleeping" function
	Bit 5	(LDA) : Low demand active (Wave 2)
	Bit 6	(PPON) : Priming pump is running
	Bit 7	(JPON) : Jockey pump is running
	Bit 8	(SLPP) : Sleep is pending
	Bit 9	(SLPB) : Sleep Boost phase is running
	Bit 10	(SLP) : Application Sleeping
	Bit 11	(ASLC) : Advanced Sleep Checking is active
ST02	Bit 0	(CNF0) : Configuration 1 is used
	Bit 8	(CFP1) : Parameter set 1 is used
	Bit 9	(CFP2) : Parameter set 2 is used
	Bit 10	(CFP3) : Parameter set 3 is used
	Bit 11	(CFP4) : Parameter set 4 is used
ST03	Bit 0	(FLO) : Drive in forced local mode
	Bit 1	(FR2) : Reference channel is channel 2
	Bit 2	(FR1B) : Reference channel is channel 1, and FR1B is used
	Bit 3	(FR1) : Reference channel is channel 1 (FR1 or FR1B is used)
	Bit 4	(CD1) : Command channel is channel 1
	Bit 5	(CD2) : Command channel is channel 2
	Bit 6	(RFC) : Reference channel : 0-> channel1, 1-> channel 2
	Bit 7	(RCB) : 0: FR1, FR1B ?
	Bit 8	(CCS) : Command channel : 0-> channel 1, 1-> channel 2
	Bit 9	(BMP) : Bump Less Channel
ST04	Bit 0	(FLT) : Drive is in fault state
	Bit 1	(AUTO) : Automatic restart attempts in progress
	Bit 2	(RDY) : Drive is in ready state
	Bit 3	(RUN) : A gating order is set (there is potentially current in the motor)
	Bit 4	(DBL) : Powerstage is supply (DC bus charged)
	Bit 5	(CTL) : Controlled stop on power loss in progress
	Bit 6	(STO) : STO function activated
	Bit 7	(FST) : A fast stop request is on going
	Bit 8	(FRF) : Reaction on event / fallback speed
	Bit 9	(RLS) : Reaction on event / maintain speed
	Bit 10	(STT) : Reaction on event / Stop on STT without tripping in fault
	Bit 11	(IDLE) : Stop and Go idle state (FF343)
ST05	Bit 0	(LLC) : The drive asks to close the line contactor
	Bit 1	(OCC) : The drive asks to close the output contactor (Controlled downstream contactor)
	Bit 2	(SOC) : The drive has detected that the output contactor is open (Uncontrolled downstream contactor)
	Bit 6	(CBEP) : The drive asks to close the circuit breaker

	Bit 7	(CBDP) : The drive asks to open the circuit breaker
	Bit 11	(DCRW) : DC Bus Ripple Warning
ST06	Bit 0	(ACC) : Drive in acceleration
	Bit 1	(DEC) : Drive in deceleration
	Bit 2	(SRA) : Frequency reference reached
	Bit 3	(MFRD) : Motor running in Forward
	Bit 4	(MRRS) : Motor running in Reverse
	Bit 5	(ATS) : Torque on the motor is negative
	Bit 6	(IDC) : DC injection is running (automatic or by order)
	Bit 9	(TUN) : Autotune in progress
	Bit 10	(MCP) : Motor current present
	Bit 11	(RP2) : Ramp switching state : 0-> ramp 1, 1-> ramp 2
	ST07	Bit 0
Bit 1		(AG2) : One of the alarm of alarm group 2 is active
Bit 2		(AG3) : One of the alarm of alarm group 3 is active
Bit 3		(AG4) : One of the alarm of alarm group 4 is active
Bit 4		(AG5) : One of the alarm of alarm group 5 is active
Bit 8		(LCA1) : Life Cycle Alarm 1 alarm
ST08	Bit 9	(LCA2) : Life Cycle Alarm 2 alarm
	Bit 0	(DRYA) : Dry Running Alarm
	Bit 1	(PLFA) : Pump Low Flow Alarm
	Bit 2	(HFPA) : High Flow Alarm
	Bit 3	(IPPA) : Inlet Pressure Protection Alarm
	Bit 4	(OPLA) : Outlet Pressure Low Alarm
	Bit 5	(OPHA) : Outlet Pressure High Alarm
	Bit 6	(PCPA) : Cyclic Start Protection Alarm
	Bit 7	(JAMA) : Antijam alarm is raised
	Bit 8	(LFA) : Low Flow Alarm
	Bit 9	(LPA) : Low pressure alarm is raised
	Bit 10	(FSA) : Flow limit function activated (FL_Mode On)
	Bit 11	(OPSA) : Outlet Pressure High Switch Alarm
	Bit 12	(MPCA) : Multi-pumps Capacity Alarm
	Bit 13	(MPLA) : Multi-pumps Lead Alarm
ST09	Bit 14	(LCHA) : Level Control High Level Alarm
	Bit 15	(LCLA) : Level Control Low Level Alarm
	Bit 0	(PFA) : alarm on PID Feedback
	Bit 1	(PFAH) : PID feedback high threshold reached
	Bit 2	(PFAL) : PID feedback low threshold reached
	Bit 3	(PIFH) : PI feedback monitoring alarm is raised
ST11	Bit 4	(LCWA) : Level Control Switch Failure Alarm
	Bit 11	(PEE) : alarm on PID error
	Bit 0	(TP2A) : Temperature Protection A12 Alarm
	Bit 1	(TP3A) : Temperature Protection A13 Alarm
	Bit 2	(TP4A) : Temperature Protection A14 Alarm
	Bit 3	(TP5A) : Temperature Protection A15 Alarm
ST12	Bit 9	(TS2A) : Temperature Sensor A12 Alarm
	Bit 10	(TS3A) : Temperature Sensor A13 Alarm
	Bit 11	(TS4A) : Temperature Sensor A14 Alarm
	Bit 12	(TS5A) : Temperature Sensor A15 Alarm
	Bit 0	(AP1) : 4-20 loss alarm on A11
	Bit 1	(AP2) : 4-20 loss alarm on A12
ST13	Bit 2	(AP3) : 4-20 loss alarm on A13
	Bit 3	(AP4) : 4-20 loss alarm on A14
	Bit 4	(AP5) : 4-20 loss alarm on A15
	Bit 0	(THA) : drive overheating alarm is raised
	Bit 1	(TJA) : alarm on IGBT thermal state
ST14	Bit 2	(FCTA) : Fan Counter Speed alarm
	Bit 3	(FFDA) : Fan feedback alarm is raised
	Bit 5	(EFA) : external alarm is raised
	Bit 6	(USA) : undervoltage alarm is raised
	Bit 7	(UPA) : controlled stop on power loss threshold id reached
	Bit 8	(OBR) : Impossible to follow the customer ramp during deceleration
	Bit 9	(URA) : Intelligent Rectifier alarm is raised
	Bit 10	(ERN) : Emergency
	Bit 0	(FTA) : Motor frequency high threshold 1 reached
	Bit 1	(FTAL) : Motor frequency low threshold 1 reached
ST15	Bit 3	(F2AL) : Motor frequency low threshold 2 reached
	Bit 4	(FLA) : HighSpeedReached function result
	Bit 5	(RTAH) : Reference frequency high threshold reached
	Bit 6	(RTAL) : Reference frequency low threshold reached
	Bit 7	(F2A) : Motor frequency high threshold 2 reached
	Bit 15	(INWM) : Ethernet Internal Warning
	Bit 0	(CTA) : Motor current high threshold reached
	Bit 1	(CTAL) : Motor current low threshold reached
ST16	Bit 2	(TTHA) : Motor torque high threshold reached
	Bit 3	(TTLA) : Motor torque low threshold reached
	Bit 4	(ULA) : Underload is detected
	Bit 5	(OLA) : Overload is detected
	Bit 6	(CLI) : current or torque limitation is running
	Bit 9	(POWD) : Electrical Power Drift alarm
	Bit 10	(PTHA) : Power high threshold reached
	Bit 11	(PTHL) : Power Low threshold reached
	Bit 1	(TAD) : DriveThermalThresholdReached function result
	Bit 2	(TSA) : MotorThermalThresholdReached function result (motor 1)
	ST17	Bit 0
Bit 1		(CAS2) : Customer Alarm 2 active
Bit 2		(CAS3) : Customer Alarm 3 active
Bit 3		(CAS4) : Customer Alarm 4 active
Bit 4		(CAS5) : Customer Alarm 5 active
ST18	Bit 0	(FCBA) : AFE : Fan running time
	Bit 1	(FFBA) : AFE : Fan frequency
	Bit 2	(CLIM) : AFE : Current or power limitation is running in motor mode
	Bit 3	(CLIG) : AFE : Current or power limitation is running in generator mode
	Bit 4	(THSA) : AFE : Sensor thermal state
	Bit 5	(THJA) : AFE : IGBT thermal state
	Bit 6	(FFCA) : MCE : Cabinet fan alarm state
	Bit 7	(FCCA) : MCE : Cabinet fan alarm running time
	Bit 8	(CHA) : MCE : Cabinet over-heating alarm
ST19	Bit 9	(CMIJ) : MCE : CMI jumper is closed
	Bit 0	(IWA) : IO function : Digital warning A
	Bit 1	(IWB) : IO function : Digital warning B
	Bit 2	(IWC) : IO function : Digital warning C
	Bit 3	(IWD) : IO function : Digital warning D
	Bit 4	(CWA) : IO function : Cabinet warning A
	Bit 5	(CWB) : IO function : Cabinet warning B
	Bit 6	(CWC) : IO function : Cabinet warning C
	Bit 7	(TWA) : IO function : Thermal input warning A
	Bit 8	(TWB) : IO function : Thermal input warning B
	Bit 9	(TWC) : IO function : Thermal input warning C
	Bit 10	(TWD) : IO function : Thermal input warning D
	Bit 11	(CBW) : Circuit breaker : Error state (not in accordance to the start and stop pulse)
ILCI	Bit 12	(P24C) : Cabinet IO supply (24V) missing
	Bit 0	"DI50" logic inputs physical image
	Bit 1	"DI51" logic inputs physical image
	Bit 2	"DI52" logic inputs physical image
	Bit 3	"DI53" logic inputs physical image
	Bit 4	"DI54" logic inputs physical image
	Bit 5	"DI55" logic inputs physical image
	Bit 6	"DI56" logic inputs physical image
	Bit 7	"DI57" logic inputs physical image
	Bit 8	"DI58" logic inputs physical image
Bit 9	"DI59" logic inputs physical image	

	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
ILCR	Bit 0	"DI50" logic inputs real image
	Bit 1	"DI51" logic inputs real image
	Bit 2	"DI52" logic inputs real image
	Bit 3	"DI53" logic inputs real image
	Bit 4	"DI54" logic inputs real image
	Bit 5	"DI55" logic inputs real image
	Bit 6	"DI56" logic inputs real image
	Bit 7	"DI57" logic inputs real image
	Bit 8	"DI58" logic inputs real image
	Bit 9	"DI59" logic inputs real image
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
OLCI	Bit 0	"R60" relay physical image
	Bit 1	"R61" relay physical image
	Bit 2	"R62" relay physical image
	Bit 3	"R63" relay physical image
	Bit 4	"R64" relay physical image
	Bit 5	"R65" relay physical image
	Bit 6	"R66" relay physical image
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
OLCR	Bit 0	"R60" relay real image
	Bit 1	"R61" relay real image
	Bit 2	"R62" relay real image
	Bit 3	"R63" relay real image
	Bit 4	"R64" relay real image
	Bit 5	"R65" relay real image
	Bit 6	"R66" relay real image
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
	Bit 12	Reserved
	Bit 13	Reserved
	Bit 14	Reserved
	Bit 15	Reserved
BPI0	Bit 0	Fault in power brick 1
	Bit 1	Fault in power brick 2
	Bit 2	Fault in power brick 3
	Bit 3	Fault in power brick 4
	Bit 4	Fault in power brick 5
	Bit 5	Fault in power brick 6
	Bit 6	Fault in power brick 7
	Bit 7	Fault in power brick 8
	Bit 8	Fault in power brick 9
	Bit 9	Fault in power brick 10
	Bit 10	Fault in power brick 11
	Bit 11	Fault in power brick 12
BFIO	Bit0	Fault in AFE brick 1
	Bit1	Fault in AFE brick 2
	Bit2	Fault in AFE brick 3
	Bit3	Fault in AFE brick 4
	Bit4	Fault in AFE brick 5
	Bit5	Fault in AFE brick 6
	Bit6	Fault in AFE brick 7
	Bit7	Fault in AFE brick 8
	Bit8	Fault in AFE brick 9
	Bit9	Fault in AFE brick 10
	Bit10	Fault in AFE brick 11
	Bit11	Fault in AFE brick 12
CIC2		On power-up, read CIC :
		If CIC and CIC2 = 0 -> Ok
		If CIC or CIC2 not 0 -> malfunction
		Write FCS = "INI" to return to factory setting
	Bit 0	= 1 : SFTY option card added
	Bit 1	= 1 : SFTY option card removed
	Bit 2	= 1 : SFTY option card changed
	Bit 3	Reserved
	Bit 4	Reserved
	Bit 5	Reserved
	Bit 6	Reserved
	Bit 7	Reserved
	Bit 8	Reserved
	Bit 9	Reserved
	Bit 10	Reserved
	Bit 11	Reserved
Bit 12	Reserved	
Bit 13	Reserved	
Bit 14	Reserved	
Bit 15	Reserved	
ST20	Bit 1	(MPDA) : Master Slave : Device loss